

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

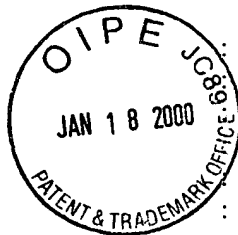
In re application of:

Tomoe Yamamoto

Appln. No.: 09/450,351

Filed: November 29, 1999

For: A METHOD FOR MANUFACTURING A
SEMICONDUCTOR DEVICE



Art Unit: To be Assigned

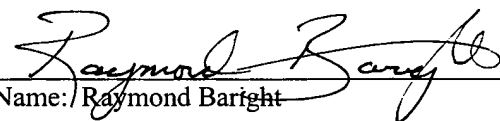
Examiner: To be Assigned

Docket No.: SHM-00901

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Respectfully submitted,
HUTCHINS, WHEELER & DITTMAR


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January 13, 2000

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